

Electronic Acknowledgement Receipt

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Application Number:	10748758
International Application Number:	
Confirmation Number:	2703
Title of Invention:	Low latency optical memory bus
First Named Inventor/Applicant Name:	Warren R. Morrow
Correspondence Address:	Jan Little-Washington BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP Seventh Floor 12400 Wilshire Boulevard Los Angeles CA 90025 US 206-292-8606 -
Filer:	Philip A. Pedigo/Derek S. Watson
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	P17981_IDS_10172007.pdf	291726 a416ac89b3013ab0f8d846cf469e0a584ffe409c	no	3

Warnings:

Information:

This is not an USPTO supplied IDS fillable form

2	NPL Documents	P17981_WO0135137A1_10172007.pdf	3273570 d2f000d532b8eb70034c6d556a199323b3c37f88	no	41
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Total Files Size (in bytes):	3565296
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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